

Electronic Acknowledgement Receipt

EFS ID:	6346305
Application Number:	09234427
International Application Number:	
Confirmation Number:	6107
Title of Invention:	INTEGRATED DIGITAL SIGNAL PROCESSOR/GENERAL PURPOSE CPU WITH SHARED INTERNAL MEMORY
First Named Inventor/Applicant Name:	AMOS INTRATER
Customer Number:	86236
Filer:	Mark C. Pickering
Filer Authorized By:	
Attorney Docket Number:	100-14299 (P01469-R1)
Receipt Date:	28-OCT-2009
Filing Date:	20-JAN-1999
Time Stamp:	14:03:23
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Certificate of Correction	P01469- R1_28OCT2009_RFCOC_LOP. pdf	145509 e5a73263f74935d946c/cae0cf35a216251fd1a	no	12

Warnings:

Information:

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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